



# SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_base = 114

SPECspeed2017\_fp\_peak = 115

CPU2017 License: 9017

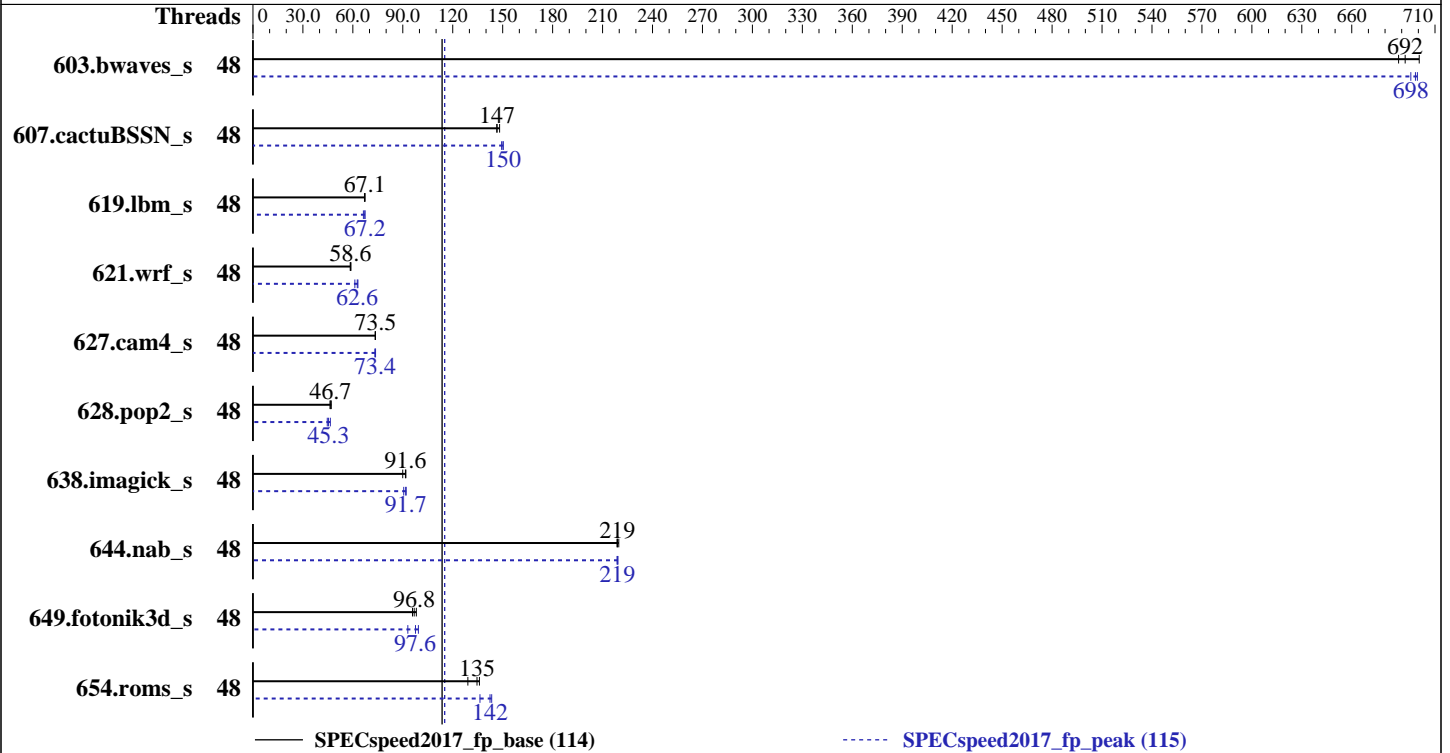
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2018

Hardware Availability: Nov-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Gold 5118  
 Max MHz.: 3200  
 Nominal: 2300  
 Enabled: 48 cores, 4 chips  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 16.5 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
 Storage: 1 x 800 GB SAS SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)  
 Kernel 3.10.0-693.el7.x86\_64  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version TEE117I 1.10 released Oct-2017  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECSpeed2017\_fp\_base = 114

SPECSpeed2017\_fp\_peak = 115

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	48	84.2	701	<u>85.2</u>	<u>692</u>	85.7	688	48	84.8	695	<u>84.5</u>	<u>698</u>	84.4	699
607.cactuBSSN_s	48	<b>114</b>	<b>147</b>	113	148	114	146	48	111	150	<u>111</u>	<u>150</u>	112	149
619.lbm_s	48	77.8	67.3	78.2	67.0	<b>78.0</b>	<b>67.1</b>	48	77.9	67.3	79.0	66.3	<b>77.9</b>	<b>67.2</b>
621.wrf_s	48	225	58.9	226	58.4	<b>226</b>	<b>58.6</b>	48	216	61.2	210	63.0	<b>211</b>	<b>62.6</b>
627.cam4_s	48	<b>121</b>	<b>73.5</b>	121	73.4	120	73.6	48	<b>121</b>	<b>73.4</b>	120	73.6	121	73.3
628.pop2_s	48	257	46.3	<b>254</b>	<b>46.7</b>	252	47.0	48	255	46.5	266	44.6	<b>262</b>	<b>45.3</b>
638.imagick_s	48	157	91.9	<b>158</b>	<b>91.6</b>	160	90.0	48	157	92.0	159	90.6	<b>157</b>	<b>91.7</b>
644.nab_s	48	79.5	220	<b>79.8</b>	<b>219</b>	79.9	219	48	79.7	219	79.9	219	<b>79.8</b>	<b>219</b>
649.fotonik3d_s	48	95.1	95.8	92.9	98.1	<b>94.1</b>	<b>96.8</b>	48	98.0	93.0	<b>93.4</b>	<b>97.6</b>	91.7	99.4
654.roms_s	48	<b>117</b>	<b>135</b>	116	136	122	129	48	115	136	110	143	<b>111</b>	<b>142</b>

SPECSpeed2017\_fp\_base = **114**

SPECSpeed2017\_fp\_peak = **115**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"  
LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"  
OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation

Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_base = 114

SPECspeed2017\_fp\_peak = 115

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

### General Notes (Continued)

generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.html>

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

### Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance  
Hyper-Threading set to Disable  
Adjacent Cache Prefetch set to Disable  
MONITORMWAIT set to Enable  
Per Core P-state set to Disable  
XPT Prefetcher set to Enable  
StaleAtoS set to Enable  
LLC deadline alloc set to Disable  
Sysinfo program /home/cpu2017.1.0.2.icl8.0/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on SR860 Fri Jan 19 10:01:55 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
 4 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 12
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
```

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_base = 114

SPECspeed2017\_fp\_peak = 115

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

### Platform Notes (Continued)

```

On-line CPU(s) list:    0-47
Thread(s) per core:    1
Core(s) per socket:    12
Socket(s):              4
NUMA node(s):          4
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  85
Model name:             Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
Stepping:               4
CPU MHz:                2300.000
BogoMIPS:               4600.00
Virtualization:         VT-x
L1d cache:              32K
L1i cache:              32K
L2 cache:               1024K
L3 cache:               16896K
NUMA node0 CPU(s):     0-11
NUMA node1 CPU(s):     12-23
NUMA node2 CPU(s):     24-35
NUMA node3 CPU(s):     36-47
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb
avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc
cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

```

```
/proc/cpuinfo cache data
cache size : 16896 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
node 0 size: 196287 MB
node 0 free: 191690 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
node 1 size: 196608 MB
node 1 free: 192233 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35
node 2 size: 196608 MB
node 2 free: 192249 MB

```

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_base = 114

SPECspeed2017\_fp\_peak = 115

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

### Platform Notes (Continued)

```

node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47
node 3 size: 196608 MB
node 3 free: 192049 MB
node distances:
node 0 1 2 3
0: 10 21 21 31
1: 21 10 31 21
2: 21 31 10 21
3: 31 21 21 10

From /proc/meminfo
MemTotal: 792256936 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
Linux SR860 3.10.0-693.el7.x86_64 #1 SMP Thu Jul 6 19:56:57 EDT 2017 x86_64 x86_64
x86_64 GNU/Linux

run-level 3 Jan 19 10:00

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 686G 121G 565G 18% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[TEE117I-1.10]- 10/19/2017
Memory:
48x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

```

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_base = 114

SPECspeed2017\_fp\_peak = 115

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

### Platform Notes (Continued)

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 619.lbm\_s(base) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CC 619.lbm\_s(peak)  
-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 607.cactuBSSN\_s(base)  
-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 607.cactuBSSN\_s(peak)  
-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)  
-----  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_base = 114

SPECspeed2017\_fp\_peak = 115

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

## Compiler Version Notes (Continued)

=====  
FC 603.bwaves\_s(peak) 649.fotonik3d\_s(peak) 654.roms\_s(peak)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

=====  
CC 621.wrf\_s(base) 627.cam4\_s(base, peak) 628.pop2\_s(base)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

=====  
CC 621.wrf\_s(peak) 628.pop2\_s(peak)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

C benchmarks:  
icc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
ifort icc

Benchmarks using Fortran, C, and C++:  
icpc icc ifort

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed2017\_fp\_base = 114

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_peak = 115

CPU2017 License: 9017

Test Date: Jan-2018

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2017

Tested by: Lenovo Global Technology

Software Availability: Sep-2017

## Base Portability Flags (Continued)

```

607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

```

Fortran benchmarks:

```

-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

```

Benchmarks using Fortran, C, and C++:

```

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

```

## Base Other Flags

C benchmarks:

-m64 -std=c11

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

(Continued on next page)





# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

SPECspeed2017\_fp\_base = 114

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_peak = 115

CPU2017 License: 9017

Test Date: Jan-2018

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2017

Tested by: Lenovo Global Technology

Software Availability: Sep-2017

## Base Other Flags (Continued)

Benchmarks using Fortran, C, and C++:

-m64 -std=c11

## Peak Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC\_SUPPRESS\_OPENMP -qopenmp  
-DSPEC\_OPENMP

638.imagick\_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC\_OPENMP

644.nab\_s: Same as 638.imagick\_s

Fortran benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -DSPEC\_SUPPRESS\_OPENMP

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed2017\_fp\_base = 114

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_peak = 115

CPU2017 License: 9017

Test Date: Jan-2018

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2017

Tested by: Lenovo Global Technology

Software Availability: Sep-2017

## Peak Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:

```
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

```
627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

628.pop2\_s: Same as 621.wrf\_s

Benchmarks using Fortran, C, and C++:

```
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch  
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs  
-align array32byte
```

## Peak Other Flags

C benchmarks:

```
-m64 -std=c11
```

Fortran benchmarks:

```
-m64
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.html>



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR860  
(2.30 GHz, Intel Xeon Gold 5118)

SPECspeed2017\_fp\_base = 114

SPECspeed2017\_fp\_peak = 115

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018

**Hardware Availability:** Nov-2017

**Software Availability:** Sep-2017

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-01-18 21:01:55-0500.

Report generated on 2018-10-31 16:54:47 by CPU2017 PDF formatter v6067.

Originally published on 2018-03-06.